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APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.
10/039,541	01/02/2002	Lawrence A. Clevenger	YOR9-2001-0508-US1	9395
28211	7590	09/07/2005	EXAMINER	
FREDERICK W. GIBB, III GIBB INTELLECTUAL PROPERTY LAW FIRM, LLC 2568-A RIVA ROAD SUITE 304 ANNAPOLIS, MD 21401			LUK, LAWRENCE W	
			ART UNIT	PAPER NUMBER
			2187	
DATE MAILED: 09/07/2005				

Please find below and/or attached an Office communication concerning this application or proceeding.

Office Action Summary	Application No.	Applicant(s)	
	10/039,541	CLEVENGER ET AL.	
	Examiner	Art Unit	
	Lawrence W. Luk	2187	

-- The MAILING DATE of this communication appears on the cover sheet with the correspondence address --

Period for Reply

A SHORTENED STATUTORY PERIOD FOR REPLY IS SET TO EXPIRE 3 MONTH(S) FROM THE MAILING DATE OF THIS COMMUNICATION.

- Extensions of time may be available under the provisions of 37 CFR 1.136(a). In no event, however, may a reply be timely filed after SIX (6) MONTHS from the mailing date of this communication.
- If the period for reply specified above is less than thirty (30) days, a reply within the statutory minimum of thirty (30) days will be considered timely.
- If NO period for reply is specified above, the maximum statutory period will apply and will expire SIX (6) MONTHS from the mailing date of this communication.
- Failure to reply within the set or extended period for reply will, by statute, cause the application to become ABANDONED (35 U.S.C. § 133). Any reply received by the Office later than three months after the mailing date of this communication, even if timely filed, may reduce any earned patent term adjustment. See 37 CFR 1.704(b).

Status

1) Responsive to communication(s) filed on 01 March 2005.

2a) This action is FINAL. 2b) This action is non-final.

3) Since this application is in condition for allowance except for formal matters, prosecution as to the merits is closed in accordance with the practice under *Ex parte Quayle*, 1935 C.D. 11, 453 O.G. 213.

Disposition of Claims

4) Claim(s) 1-20 is/are pending in the application.
4a) Of the above claim(s) _____ is/are withdrawn from consideration.

5) Claim(s) _____ is/are allowed.

6) Claim(s) 1-20 is/are rejected.

7) Claim(s) _____ is/are objected to.

8) Claim(s) _____ are subject to restriction and/or election requirement.

Application Papers

9) The specification is objected to by the Examiner.

10) The drawing(s) filed on _____ is/are: a) accepted or b) objected to by the Examiner.

Applicant may not request that any objection to the drawing(s) be held in abeyance. See 37 CFR 1.85(a).

Replacement drawing sheet(s) including the correction is required if the drawing(s) is objected to. See 37 CFR 1.121(d).

11) The oath or declaration is objected to by the Examiner. Note the attached Office Action or form PTO-152.

Priority under 35 U.S.C. § 119

12) Acknowledgment is made of a claim for foreign priority under 35 U.S.C. § 119(a)-(d) or (f).
a) All b) Some * c) None of:
1. Certified copies of the priority documents have been received.
2. Certified copies of the priority documents have been received in Application No. _____.
3. Copies of the certified copies of the priority documents have been received in this National Stage application from the International Bureau (PCT Rule 17.2(a)).

* See the attached detailed Office action for a list of the certified copies not received.

Attachment(s)

1) Notice of References Cited (PTO-892) 4) Interview Summary (PTO-413)
2) Notice of Draftsperson's Patent Drawing Review (PTO-948) Paper No(s)/Mail Date. ____ .
3) Information Disclosure Statement(s) (PTO-1449 or PTO/SB/08)
 Paper No(s)/Mail Date ____ . 5) Notice of Informal Patent Application (PTO-152)
6) Other: ____ .

DETAILED ACTION

1. The indicated allowability of claims 5 and 7-10 are withdrawn in view of the newly discovered reference(s) to Bond et al. (5,724,728); Matsuura (6,673,484). The instant application having Application No. 10/039,541 has a total of 20 claims pending in the application; there are 6 independent claims and 14 dependent claims, all of which are rejections based on the newly cited reference(s) follow.

Claim Rejections - 35 USC § 102

2. The following is a quotation of the appropriate paragraphs of 35 U.S.C. 102 that form the basis for the rejections under this section made in this Office action:

A person shall be entitled to a patent unless –

(b) the invention was patented or described in a printed publication in this or a foreign country or in public use or on sale in this country, more than one year prior to the date of application for patent in the United States.

3. Claims 1-5 and 7-10 are rejected under 35 U.S.C. 102(b) as being anticipated by Bond et al. (5,724,728).

Claim 1

As to claim 1, Bond et al. disclose in **figure 1B**, a system level device for battery and integrated circuit chip integrated circuit chip integration comprising: at least one battery **(28)**; at least one integrated circuit chip **(18)** powered by said at least one battery **(28)**; and a package **(12)** having a pair of opposed upright ends, said package **(12)** connected to any of said at least one battery **(28)** and said at least one integrated circuit chip **(18)**, wherein said at least one integrated circuit chip **(18)** lays on top of a portion of said package **(12)**, and wherein said at least one battery **(28)** overhangs said at least

one integrated circuit chips (18). (see column 3, lines 55-67 and column 4, lines 35-46).

Claim 2

As to claim 2, Bond et al. disclose in **figure 1B**, wherein said package (12) connects to said at least one integrated circuit chip (18) through an interior portion of said package (12). (see column 4, lines 6-8).

Claim 3

As to claim 3, Bond et al. disclose in **figure 1B**, wherein said at least one integrated circuit chip (18) connects to an upper indent portion of said package (14); wherein said at least one battery (28) is larger than said at least one integrated circuit chip (18). (see column 4, lines 17-23)

Claim 4

As to claim 4, Bond et al. disclose in **figure 1B**, wherein said at least one battery (28) connects to an underside of said package (12). (see column 4, lines 38-46)

Claim 5

As to claim 5, Bond et al. disclose in **figure 1B**, a system level device for battery and integrated circuit chip integration comprising; at least one battery (28); at least one integrated circuit chip (18) powered by said at least one battery (28); and a package (12) connected to any of said at least one battery (28) and said at least one integrated circuit chip (18), wherein said at least one battery (28) connects to a pair of opposed upright ends of said package (12), wherein said at least one integrated circuit chip (18) is disposed between said at least one battery (28) and said package (12), and wherein

said at least one integrated circuit chip (18) lays on top of a portion of said package (12). (see column 3, lines 55-67 and column 4, lines 35-46).

Claim 7

As to claim 7, Bond et al. disclose in **figure 1B**, a system level device for battery and integrated circuit chip integration comprising: a multi-chip module (18, **see column 3, lines 63-67**) integration system, wherein said multi-chip module integration system (10) comprises: a multi-chip module (18 and 12) having a pair of opposed upright ends; at least one battery (28) connected to said multi-chip module (18 and 12); and at least one integrated circuit chip (18) connected to said battery (28), wherein said integrated circuit chip (18) is powered by said battery (28), and wherein said at least one battery (28) overhang, and is larger than, said at least one integrated circuit chip (18), wherein said at least one integrate circuit chip (18) lays on top of a portion of said multi-chip module (18 and 12). (see column 3, lines 55-67 and column 4, lines 35-46).

Claim 8

As to claim 8, Bond et al. disclose in **figure 1B**, wherein said multi-chip module (18 and 12) connects to said at least one integrated circuit chip (18) through an interior portion of said multi-chip module (18 and 12). (see column 3, lines 63-67).

Claim 9

As to claim 9, Bond et al. disclose in **figure 1B, 4A, 4B**, wherein said at least one integrated circuit chip (18) connects (20, 26a, 26b, 28, 32a, 32b) to an upper indent portion of said multi-chip module (18 and 12). (see column 3, lines 63-67).

Claim 10

As to claim 10, Bond et al. disclose in **figure 1B**, a system level device for battery and integrated circuit chip integration comprising a multi-chip module integration system (**18, see column 3, lines 63-67**), wherein said multi-chip module integration system comprises: a multi-chip module (**18 and 12, see column 3, lines 60-62**); at least one battery (**28**) connected to said multi-chip module (**18 and 12**); and at least one integrated circuit chip (**18**) connected to said battery (**28**), wherein said integrated circuit chip (**18**) is powered by said battery (**28**), wherein said at least one battery (**28**) connects to a pair of opposed upright ends of said multi-chip module (**18 and 12**), wherein said at least one battery (**28**) overhangs, and is larger than, said at least one integrated circuit chip (**18**), and wherein said at least one integrated circuit chip (**18**) lays on top of a portion of said multi-chip module (**18 and 12**). (**see column 3, lines 55-67 and column 4, lines 35-46**).

4. The following is a quotation of the appropriate paragraphs of 35 U.S.C. 102 that form the basis for the rejections under this section made in this Office action:

A person shall be entitled to a patent unless –

(a) the invention was known or used by others in this country, or patented or described in a printed publication in this or a foreign country, before the invention thereof by the applicant for a patent.

5. Claim 11 is rejected under 35 U.S.C. 102(a) as being anticipated by Miyata (JP02001015673 A).

Claim 11

As to claim 11, Miyata disclose in **figure 2**, an integrated chip structure comprising: an integrated circuit chip **(6)**; a battery **(8)** directly connected **(4)** to said integrated circuit chip **(6)** and a package **(1)** having a pair of opposed upright ends, said package **(1)** connected to any of said battery **(8)** and said integrated circuit chip **(6)**, wherein said integrated circuit chip **(6)** lays on top of a portion of said package **(1)**, and wherein said at least one battery **(8)** overhangs said at least one integrated circuit chip **(6)**. **(see Abstract)**.

6. The following is a quotation of the appropriate paragraphs of 35 U.S.C. 102 that form the basis for the rejections under this section made in this Office action:

A person shall be entitled to a patent unless –

(e) the invention was described in (1) an application for patent, published under section 122(b), by another filed in the United States before the invention by the applicant for patent or (2) a patent granted on an application for patent by another filed in the United States before the invention by the applicant for patent, except that an international application filed under the treaty defined in section 351(a) shall have the effects for purposes of this subsection of an application filed in the United States only if the international application designated the United States and was published under Article 21(2) of such treaty in the English language.

7. Claims 16-19 are rejected under 35 U.S.C. 102(e) as being anticipated by Matsuura (6,673,484).

Claim 16

As to claim 16, Matsuura disclose in **figure 2A, 2B**, an integrated chip structure comprising: a package **(20)** having a pair of opposed upright ends; an integrated circuit chip **(12)** mounted on said package **(20)**; a battery **(40)** directly connected to said package **(20, see column 4, lines 30-46)** and electrically connected to said integrated

circuit chip (12), wherein said integrated circuit chip (12) lays on top of a portion or said package (20), and wherein said at least one battery (40) overhangs said at least one integrated circuit chip (12). (see column 4, lines 16-46).

Claim 17

As to claim 17, Matsuura disclose in **figure 2A, 2B**, wherein said battery (40) is held adjacent to said integrated circuit chip (12) by said package (20). (see column 4, lines 29-46)

Claim 18

As to claim 18, Matsuura disclose in **figure 4A**, wherein said package (20) is between said battery (40) and said integrated circuit chip (12). (see column 6, lines 16-32)

Claim 19

As to claim 19, Matsuura disclose in **figure 4A**, wherein said battery (40) is electrically connected to said integrated circuit chip (12) through said package (20). (see column 6, lines 28-32)

Claim Rejections - 35 USC § 103

8. The following is a quotation of 35 U.S.C. 103(a) which forms the basis for all obviousness rejections set forth in this Office action:

(a) A patent may not be obtained though the invention is not identically disclosed or described as set forth in section 102 of this title, if the differences between the subject matter sought to be patented and the prior art are such that the subject matter as a whole would have been obvious at the time the invention was made to a person having ordinary skill in the art to which said subject matter pertains. Patentability shall not be negatived by the manner in which the invention was made.

9. Claim 20 is rejected under 35 U.S.C. 103(a) as being unpatentable over Matsuura (6,673,484) in view of Tuttle (6,548,207).

Claim 20

As to claim 20, Matsuura disclose the elements as claimed except Matsuura fails to teach the limitation of **wherein said battery comprises multiple batteries stacked on said package.**

Tuttle disclose in figure 2 and 8, wherein said battery comprises multiple batteries (32, 28) stacked on said package (16). (see column 1, lines 60-61, column 4, lines 15-16).

Matsuura and Tuttle are analogous art because they are from same field of endeavor of the electric devices to be mounted on the circuit board include an integrated circuit and a battery cell.

At the time of the invention it would have been obvious to a person of ordinary skill in the art to include multiple batteries stacked on said package

The suggestion/motivation for doing so would have been to provides a particular button-type battery circuits and constructions, which have enhanced power production capabilities and/or battery lifetimes. (see column 1, lines 32-37 of Tuttle).

Therefore, it would have been obvious to combine Tuttle with Matsuura for the multiple batteries stacked on said package to obtain the invention as specified in claim 20.

10. Claims 12, 13 and 15 are rejected under 35 U.S.C. 103(a) as being unpatentable over Miyata (JP02001015673A) in view of Levy (4,026,304).

Claims 12 and 13

As to claims 12 and 13, Miyata disclose the elements as claimed except Miyata fails to teach the limitation of **further comprising solder connections between said battery and said integrated circuit chip.**

Levy disclose in figure 1 and 2, further comprising solder connections between said battery and said integrated circuit chip. (**see column 6, lines 6-10**).

Miyata and Levy are analogous art because they are from same field of endeavor of the electric devices to be mounted on the circuit board including an integrated circuit and a battery cell.

At the time of the invention it would have been obvious to a person of ordinary skill in the art to include solder connections between said battery and said integrated circuit chip.

The suggestion/motivation for doing so would have been to provides a construction process using conventional soldering techniques (**see column 6, lines 8-10 of Levy**).

Therefore, it would have been obvious to combine Levy with Miyata for forming the electrical connection between the battery and the integrated circuit chip as specified in claim 12-13.

Claim 14

As to claim 14, Miyata in view of Levy are applied supra, and Miyata further disclose in **figure 2**, said wherein said package (1) surrounds said battery (8) and said integrated circuit chip (6).

Claim 15

As to claim 15, Miyata in view of Levy are applied supra, and Miyata further disclose in figure 2, said battery (8) is directly connected (5) to said package (20). (**see Figure 2 and Abstract**).

11. Claim 6 is rejected under 35 U.S.C. 103(a) as being unpatentable over Bond et al. (5,724,728) in view of Tuttle (6,548,207).

Claim 6

As to claim 6, Bond et al. disclose the elements as claim 3, except Bond et al. fails to teach the limitation of **a stack of connected batteries**.

Tuttle disclose in figure 2 and 8, wherein said battery comprises a stack of connected batteries (32, 28). (**see column 1, lines 60-61, column 4, lines 15-16**).

Bond et al. and Tuttle are analogous art because they are from same field of endeavor of the electric devices to be mounted on the circuit board to include an integrated circuit and a battery cell.

At the time of the invention it would have been obvious to a person of ordinary skill in the art to include a stack of connected batteries.

The suggestion/motivation for doing so would have been to provide a particular button-type battery circuits and constructions, which have enhanced power production capabilities and/or battery lifetimes. (see column 1, lines 32-37 of Tuttle).

Therefore, it would have been obvious to combine Tuttle with Bond et al. for a stack of connected batteries to obtain the invention as specified in claim 20.

12. RELEVANT ART CITED BY THE EXAMINER

The following prior art made of record and not relied upon is cited to establish the level of skill in the applicant's art and those arts considered reasonably pertinent to applicant's disclosure.

See MPEP 707.05 (c).

The following references teach the electric devices to be mounted on the circuit board include an integrated circuit and a battery cell.

<u>US PATENT NUMBER</u>	<u>FIGURES</u>
6,906,407	2-4
6,459,593	2
5,963,429	3
5,498,903	1, 3-5
4,381,458	1, 3

<u>FOREIGN PATENT NUMBER</u>	<u>FIGURES</u>
FR 2822572	1-3
DE 19612718	1, 3, 4

Response to Arguments

13. Applicant's arguments with respect to claims 1-20 have been considered but are moot in view of the new ground(s) of rejection.

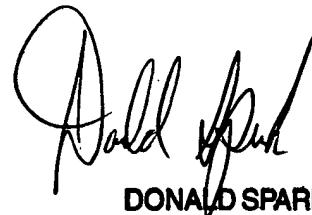
Conclusion

14. Any inquiry concerning this communication or earlier communications from the examiner should be directed to Lawrence W Luk whose telephone number is (571) 272-2080. The examiner can normally be reached on 7 a.m. to 5 p.m..

If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, Donald A Sparks can be reached on (571) 272-4201. The fax phone number for the organization where this application or proceeding are (703) 746-7239, (571) 272-2100 for regular communication and (703) 746-7238 for After Final communications.

Any inquiry of a general nature or relating to the status of this application or proceeding should be directed to receptionist whose telephone number is (571) 272-2100.

LWL
August 25, 2005



DONALD SPARKS
SUPERVISORY PATENT EXAMINER